

# 2SK3512-01L,S,SJ

**FUJI POWER MOSFET** 

### **Super FAP-G Series**

#### ■ Features

High speed switching Low on-resistance No secondary breadown Low driving power Avalanche-proof

#### Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

#### ■ Maximum ratings and characteristic

#### ◆ Absolute maximum ratings (T<sub>c</sub>=25°C unless otherwise specified)

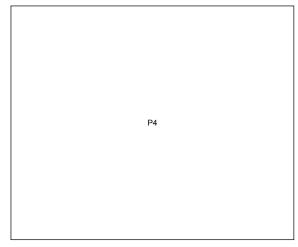
Items	Sym	ibol	Ratings	Units	
Drain-source voltage	VDS		500	V	
Continuous drain current	ΙD		±14	Α	
Pulsed drain current	I <sub>D (puls)</sub>		±56	Α	
Gate-source voltage	V <sub>GS</sub>		±30	V	
Repetitive or non-repetitive	I <sub>AR</sub> (*2)		14	Α	
Maximum Avalanche Energy	Eas (	(*1)	188.2	mJ	
Maximum Drain-Source dV/dt	dV <sub>DS</sub> /dt (*4)		20	kV/μs	
Peak Diode Recovery dV/dt	dV/c	lt (*3)	5	kV/μs	
Max. power dissipation	P₀	T <sub>a</sub> =25°C	1.67	W	
		Tc=25°C	195	VV	
Operating and storage			+150	°C	
temperature range	T <sub>stg</sub>		-55 to +150	°C	

Note \*1: L=2.77mH, Vcc=50V Note \*2: Tch≦150°C

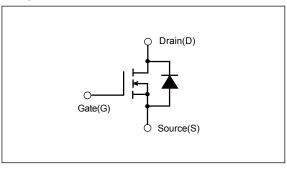
Note \*3: Ir≦-Ib₀, -di/dt=50A/μs, Vcc≦BV<sub>DSS</sub>, T<sub>ch</sub>≦150°C Note \*4: V<sub>DS</sub>≦500V

### **N-CHANNEL SILICON POWER MOSFET**

#### Outline Drawings



#### ■ Equivalent circuit schematic



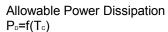
#### ● Electrical characteristics (T<sub>c</sub> =25°C unless otherwise specified)

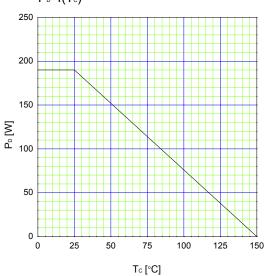
Items	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain-source breakdown voltaget	V <sub>(BR)DSS</sub>	I <sub>D</sub> =250µA V <sub>GS</sub> =0V	500			V
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> = 250µA V <sub>DS</sub> =V <sub>GS</sub>	3.0		5.0	V
Zero gate voltage drain current		V <sub>DS</sub> =500V V <sub>GS</sub> =0V T <sub>ch</sub> =25°C			25	μA
	IDSS	V <sub>DS</sub> =400V V <sub>GS</sub> =0V T <sub>ch</sub> =125°C			250	
Gate-source leakage current	Igss	V <sub>GS</sub> =±30V V <sub>DS</sub> =0V		10	100	nA
Drain-source on-state resistance	R <sub>DS(on)</sub>	I <sub>D</sub> =6A V <sub>GS</sub> =10V		0.40	0.52	Ω
Forward transcondutance	g <sub>fs</sub>	I <sub>D</sub> =6A V <sub>DS</sub> =25V	5.5	11		S
Input capacitance	Ciss	V <sub>DS</sub> =25V		1200	1800	pF
Output capacitance	Coss	V <sub>GS</sub> =0V		140	210	
Reverse transfer capacitance	Crss	f=1MHz		6.0	9.0	
Turn-on time ton	t <sub>d(on)</sub>			17	26	ns
	tr	V <sub>cc</sub> =300V I <sub>D</sub> =6A - V <sub>GS</sub> =10V		15	23	
Turn-off time toff	t <sub>d(off)</sub>	R <sub>GS</sub> =10 V		34	51	
	<b>t</b> f	1.65 1.6 1.		7	11	
Total Gate Charge	Q <sub>G</sub>	Vcc=250V		30	45	nC
Gate-Source Charge	Q <sub>GS</sub>	I₀=12A		11	16.5	
Gate-Drain Charge	Q <sub>GD</sub>	V <sub>GS</sub> =10V		10	15	
Avalanche capability	I <sub>AV</sub>	L=1.76mH T <sub>ch</sub> =25°C	14			Α
Diode forward on-voltage	V <sub>SD</sub>	I <sub>F</sub> =12A V <sub>GS</sub> =0V T <sub>ch</sub> =25°C		1.00	1.50	V
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> =12A V <sub>GS</sub> =0V		0.7		μs
Reverse recovery charge	Qn	-di/dt=100A/µs Tch=25°C		4.5		μC

#### Thermal Characteristics

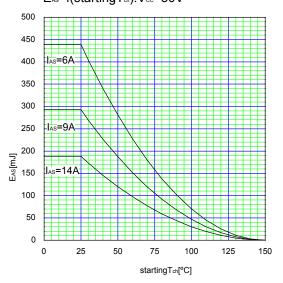
Items	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Thermal resistance	R <sub>th(ch-c)</sub>	channel to case			0.641	°C/W
	R <sub>th(ch-a)</sub>	channel to ambient			75.0	°C/W

#### ■ Characteristics

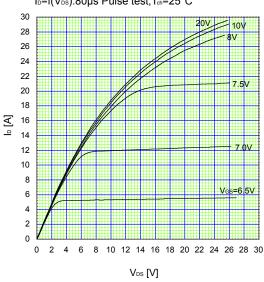




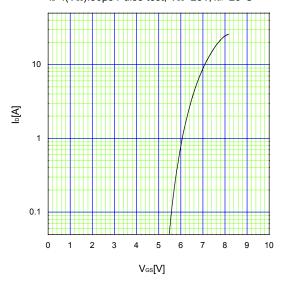
#### Maximum Avalanche Energyvs. starting Tch $E_{As}=f(startingT_{ch}):V_{CC}=50V$



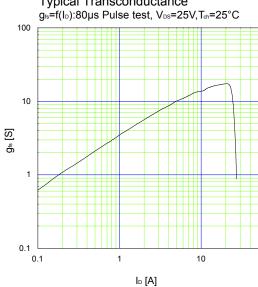
#### **Typical Output Characteristics** I<sub>D</sub>=f(V<sub>DS</sub>):80µs Pulse test,T<sub>ch</sub>=25°C



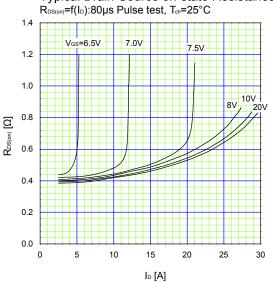
Typical Transfer Characteristic  $I_D=f(V_{GS}):80\mu s$  Pulse test,  $V_{DS}=25V,T_{ch}=25^{\circ}C$ 



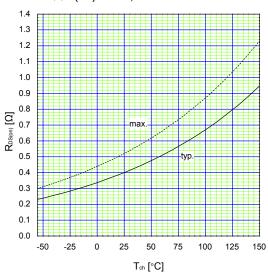
#### Typical Transconductance



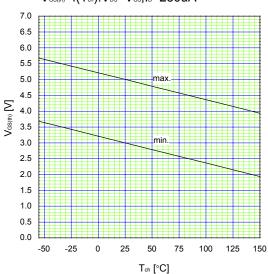
Typical Drain-Source on-state Resistance



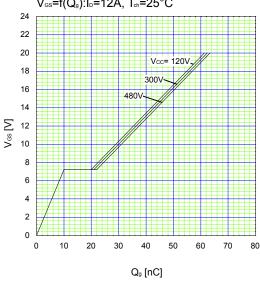
Drain-Source On-state Resistance  $R_{DS(on)} = f(T_{ch}): I_D = 6A, V_{cs} = 10V$ 



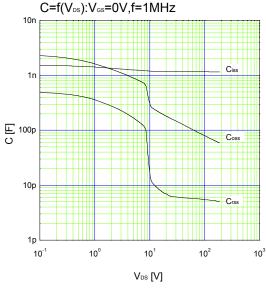
Gate Threshold Voltage vs.  $T_{ch}$   $V_{GS(th)}$ = $f(T_{ch})$ : $V_{DS}$ = $V_{GS}$ , $I_D$ =250uA



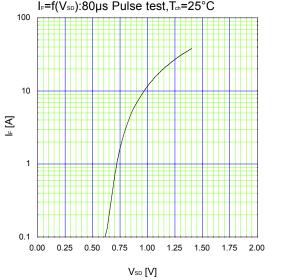
Typical Gate Charge Characteristics  $V_{\text{GS}}$ =f(Q<sub>g</sub>):I<sub>D</sub>=12A, T<sub>ch</sub>=25°C



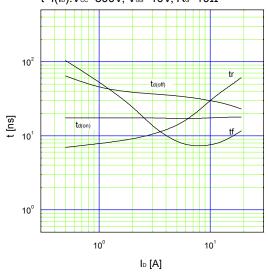
Typical Capacitance



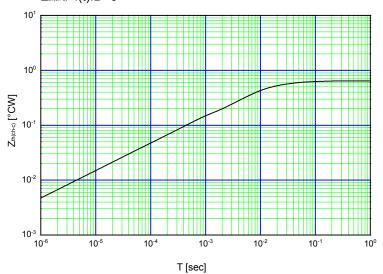
Typical Forward Characteristics of Reverse Diode  $I_F=f(V_{SD}):80\mu s$  Pulse test,  $T_{SD}=25^{\circ}C$ 



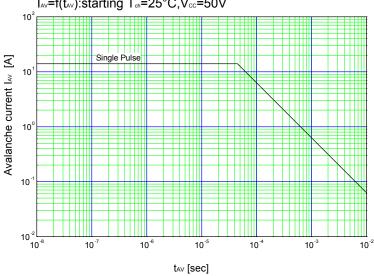
Typical Switching Characteristics vs.  $I_D$  t=f( $I_D$ ): $V_{CC}$ =300V,  $V_{CS}$ =10V,  $R_C$ =10 $\Omega$ 



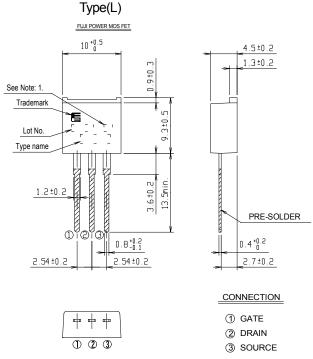
## Maximum Transient Thermal Impedance $Z_{\mbox{\tiny In(ch-c)}} \! = \! f(t) \! : \! D \! = \! 0$



## Maximum Avalanche Current vs Pulse width $I_{\text{AV}} \! = \! f(t_{\text{AV}}) \! : \! starting \ T_{\text{ch}} \! = \! 25^{\circ} C, V_{\text{CC}} \! = \! 50V$

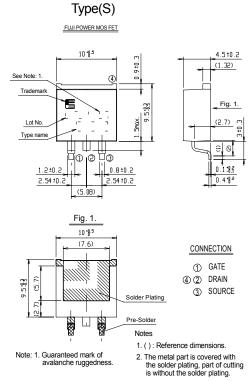


#### ■ Outline Drawings [mm]

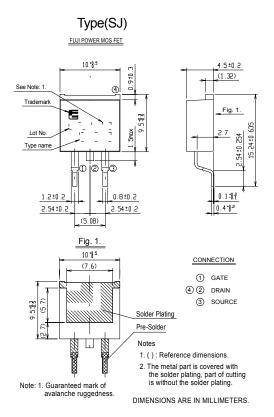


Note: 1. Guaranteed mark of avalanche ruggedness.

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